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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/810,117	03/25/2004	James Schleicher	306812005700	8048	
47520	7590 07/26/2005		EXAM	INER	
ALTERA c/o MOFO SF		·	CHO, JAMES HYONCHOL		
425 MARKET SAN FRANC	r STREET ISCO, CA 94105		CHO, JAMES HYONCHOL ART UNIT PAPER NUMBER		
	,		2819	•	

DATE MAILED: 07/26/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

				- 4 /
		Application No.	Applicant(s)	
		10/810,117	SCHLEICHER ET A	L.
Office Action	n Summary	Examiner	Art Unit	
		James Cho	2819	
The MAILING DAT Period for Reply	E of this communication app	ears on the cover sheet with the	correspondence addi	ress
A SHORTENED STATUTHE MAILING DATE OF Extensions of time may be availar after SIX (6) MONTHS from the lifthe period for reply specified at If NO period for reply is specified. Failure to reply within the set or experiments.	THIS COMMUNICATION. able under the provisions of 37 CFR 1.1: mailing date of this communication. bove is less than thirty (30) days, a reply above, the maximum statutory period vextended period for reply will, by statute later than three months after the mailing	Y IS SET TO EXPIRE 3 MONTH 36(a). In no event, however, may a reply be to within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from cause the application to become ABANDON of date of this communication, even if timely file	imely filed ays will be considered timely. the mailing date of this com ED (35 U.S.C. § 133).	munication.
Status				
1) Responsive to com	nmunication(s) filed on 25 M	arch 2004.		
2a) This action is FINA	L. 2b)⊠ This	action is non-final.	•	
•		nce except for formal matters, per parte Quayle, 1935 C.D. 11, 4		nerits is
Disposition of Claims				
4) ⊠ Claim(s) <u>1-24</u> is/ard 4a) Of the above classified is the second of the above classified is the second of t	/are rejected.	vn from consideration.		
Application Papers				
9)⊠ The specification is	objected to by the Examine	r.		
10)⊠ The drawing(s) filed	on <u>25 March 2004</u> is/are: a	a)∏ accepted or b)⊠ objected	to by the Examiner.	
Applicant may not red	quest that any objection to the	drawing(s) be held in abeyance. Se	ee 37 CFR 1.85(a).	
		ion is required if the drawing(s) is o aminer. Note the attached Offic	-	• •
Priority under 35 U.S.C. § 1	19			
a) All b) Some 1. Certified cop 2. Certified cop 3. Copies of the application fr	* c) None of: ies of the priority documents ies of the priority documents e certified copies of the prior com the International Bureau	s have been received in Applica ity documents have been receiv	tion No red in this National S	tage
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Attachment(s)				
 Notice of References Cited (P Notice of Draftsperson's Pate 	TO-892) nt Drawing Review (PTO-948)	4) Interview Summar Paper No(s)/Mail D		
 Notice of Draitsperson's Pale Information Disclosure Statem Paper No(s)/Mail Date 1/05. 			Patent Application (PTO-1	52)

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DETAILED ACTION

Drawings

The drawings are objected to because lines, numbers & letters in Figs. 1 - 4 are not uniformly thick and well defined, clean, durable, and black. 37 CFR 1.84(I). A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

Specification

The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

The following title is suggested: --Omnibus Logic Element including Look-up
Table-base Logic Elements--.

Claim Objections

Claims 1 and 3 are objected to because of the following informalities:

In claim 1, "A plurality" on line 2 appears to be --a plurality--;

In claim 3, "MUX the first" on line 1 appears to be --MUX, the first--.

Appropriate correction is required.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States

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only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-2 are rejected under 35 U.S.C. 102(e) as being anticipated by Pedersen (US PAT No. 6,798,240).

Regarding claim 1, Fig. 5 of Pedersen teaches a logic circuit including; a plurality of look up tables (530s and 550s) driven by a plurality of inputs (A1, A2, B1, B2, C1, C2), each of the plurality of LUTS included either in a first group of LUTS (530s) or a second group of LUTS (550s); at least one (A1) of the plurality of inputs driving each of the plurality of LUTS; at least a second (A2) of the plurality of inputs driving each LUT in the first group of LUTS and connectable to drive at least one of the LUTS in the second group of LUTS (550); at least a third (B1) of the plurality of inputs connectable to drive at least one of the LUTS in the first group of LUTS; at least two LUTS (two 530) in the first group of LUTS each driving a first multiplexer (570) and a second MUX (second 570); and at least a fourth (signals at either 506a or 506b) of the plurality of inputs driving a control input of the first MUX.

Regarding claim 2, Fig. 5 of Pedersen teaches the logic circuit of claim 1 including; at least a fifth input (B2) driving each LUT in the second group of LUTS and connectable to drive at least one of the LUTS in the first group of LUTS (505 receives B2 via 535, col. 13, lines 29-34), at least a sixth input (C2 via 555) of the plurality of inputs connectable to drive at least one of the LUTS in the second group of LUTS; at least two LUTS in the second group of LUTS each driving a third MUX (590) and a

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fourth MUX (590); and at least a seventh input (Vcc via 516b) of the plurality of inputs driving a control input of the third MUX.

Allowable Subject Matter

Claims 16-24 are allowable over the prior art of record.

Claims 3-15 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter; one of ordinary skill in the art would not have been motivated to modify the teaching of Pedersen to further includes, among other things, the specifics of the first input MUX driven by the at least third input and the at least fifth input where the first input MUX drives at least one of the LUTS in the first group of LUTS and the control input of the second MUX and the second input MUX drives at least one of the LUTS in the second group of LUTS and the control input of the fourth MUX as required by claim 3, the specifics of the first group of LUTs and the second group of LUTs including a 4input LUT and two 3-input LUTs respectively, the specifics of at least a first adder and a second adder where the first adder is driven by one of the first plurality of LUTS and at least one of either one of the first plurality of multiplexers or a share-in input to the logic circuit, and the second adder is driven by one of the second plurality of LUTS and one of either one of the second plurality of multiplexers or one of the first plurality of multiplexers as required by claim 16, and the specifics of the first configuration carrying out a first 6-input logic function along with a second 6-input logic function, the first 6Application/Control Number: 10/810,117 Page 5

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input logic function sharing at least 4 inputs with the second 6-input logic function as required by claim 23.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Young et al. (US PAT No. 6,124,731) discloses a configurable logic element with ability to evaluate wide logic functions.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to James Cho whose telephone number is 571-272-1802. The examiner can normally be reached on M-F 6:30 AM - 3:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mike Tokar can be reached on 571-272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

James H. Cho Primary Examiner

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7/23/2005